

1. IDENTIFICATION
- 1.1 Maindec 712
- 1.2 PDP-7 Tape Punch (Type 75D) Test
- 1.3 July 22, 1965

(7-81-m)



2. ABSTRACT

Maindec 712 incorporates three separate programs for testing the functions of the paper tape punch. All three programs are in the computer at the same time. The first program checks the 1-second delay for proper operation when the initial Punch Select IOT is given. The second program is designed for use with a CRT. It causes the contents of the AC Switches to be punched continuously on tape in either binary or alphanumeric modes. The third program causes the punch to produce a tape containing a sequence of pseudo-random numbers; this tape is then checked using the tape reader.

3. REQUIREMENTS

3.1 Storage

Maindec 712 occupies memory registers 00000-01423 including output subroutines.

3.2 Subprograms

Maindec 712 uses the Teletype Output Package, Digital-7-10-0, and the Octal Print Subroutine, Digital-7-14-0. Both of these subroutines are included on the program tape.

3.3 Equipment

Standard PDP-7 with Paper Tape Reader and Punch

3.4 Miscellaneous

The following tapes are supplied:

ASCII (2, labeled A and D
FF

ASCII Tapes of the two library subroutines (see Section 3.2) are not provided.

4. USAGE

4.1 Loading

The RIM Loader must be in memory.

4.1.1 Set the ADDRESS switches to 17770.

4.1.2 Place the FF Program tape in the reader.

4.1.3 Press START.

4.3 Switch Settings

4.3.1 Loading Address

17770

4.3.2 Starting Addresses

00040 (Part 1)
00100 (Part 2)
00200 (Part 3)

*207 } NOP eliminates punch print
210 }
213 }*

*251 } NOP eliminates code, print
252 }
253 }*

255 STARTS PRINT-OUT READING WITH NO

4.3.3 Other settings

The table below gives the various AC switch settings for Parts 2 and 3 of the Punch Test. Part 1 does not use the ACS.

Test	Switches	Setting	Function
Part 2	ACS ₀	down up	Punch alphanumeric. Punch binary.
Part 2	ACS ₁	down up	Punch character true. Alternate true character and complement.
Part 2	ACS ₁₀₋₁₇	---	Setting determines character to be punched.
Part 3	ACS ₀	down up	Halt on error. Do not halt on error.
Part 3	ACS ₁	down up	Print error messages. Do not print error messages.
Part 3	ACS ₁₇	down up	Punch random sequence. Terminate random punch and prepare for reader check.

4.4 Start up and Operation

4.4.1 Part 1 -- Startup Delay Test

Set the ADDRESS switches to 00040 and press START. The program title will be printed on the Teleprinter: ←

STARTUP DELAY TEST

Turn on punch power at console.

If the test is successful the message

DELAY O.K. TURN OFF PUNCH POWER SWITCH

is printed, and the computer halts with all 1's displayed in the AC lights.

4.4.2 Part 2 -- Scope Waveform Test

Set the ADDRESS switches to 00100 and press START. The program title and instructions are printed:

SCOPE WAVEFORM TEST.

ACS₀ UP FOR BINARY, DOWN FOR ALPHA.

ACS₁ UP IF COMPLEMENTING, DOWN IF NOT.

ACS₁₀₋₁₇ FOR CHARACTER PUNCHED.

and the computer halts with the AC clear.

Set the ACS as desired, and press CONTINUE. The ACS setting can be changed at any time during the execution of the test; the program will respond immediately to the new setting. There is no final halt in this part; the operator must stop the computer manually.

4.4.3 Part 3--Random Sequence Punch

Set the ACS to 00200 and press START. The program title and instructions for the punch section of the test are printed:

```
RANDOM SEQUENCE PUNCH ROUTINE
ACS0 DOWN FOR ERROR HALTS, UP FOR NONE.
ACS1 DOWN FOR ERROR PRINT, UP FOR NONE.
ACS17 DOWN TO START. PUT UP TO STOP PUNCHING.
```

The computer halts with the AC and link clear.

Set the ACS in accordance with the instructions, and press CONTINUE. When as much tape as is desired has been produced, raise AC switch 17. The random sequence will stop, a short length of blank trailer is punched, and the title and instructions for the reader check are printed:

```
READER CHECK OF RANDOM TAPE
ACS0 AND ACS1 AS FOR PUNCH. ERRORS PRINTED BELOW.
CHARACTER
PUNCHED EXPECTED
```

The computer halts with the AC and link clear. Remove the random sequence tape from the punch bin, and place it in the reader so that the blank leader falls under the read heads. Set the ACS as desired, and press CONTINUE. Errors will be printed in columns under the headings PUNCHED and EXPECTED. When the whole tape has been scanned, the program comes to a final halt with the AC lights displaying all 1's.

4.5 Errors

Errors E00, E20, and E21 will always halt, regardless of the setting of ACS₀. All other errors will stop only if ACS₀ is down.

4.5.1 Error stops in Part 1

Error: E00
Message: STARTUP DELAY FAILURE. PRESS CONTINUE TO TRY AGAIN.
C(MA): 00064
C(AC): Time left in clock
Cause: The punch flag was set before about 1.1 seconds elapsed. This indicates a failure of the one-shot delay which prevents the first select instruction from taking effect until the drive clutch has been engaged. The AC contains the complement of the contents of register 00007. This number is the octal equivalent of the time remaining when the flag was set, in 60ths of a second.
Recovery: Press CONTINUE. The test will start over again.

4.5.2 Error stops in Part 2.

There are no error stops in this part of the test.

4.5.3 Error stops in Part 3.

Error: E01
Message: PSA+10 DID NOT CLEAR AC.
C(MA): 00153

C(AC): I/O status
Cause: The IOT instruction PSA+10 failed to clear the AC before selecting the punch. This test is made only when punching the blank leader and trailer on the test tape.
Recovery: Press CONTINUE. The test will proceed from the point of the error.

Error: E10
Message: CAF DID NOT CLEAR PUNCH FLAG.
C(MA): 00216
C(AC): I/O status
Cause: The punch flag was unaffected by the CAF instruction. This error implies that the punch flag is permanently on, since at any time prior to this point in the program, pressing START should have cleared the flag.
Recovery: Press CONTINUE. The test will proceed from the point of the error.

Error: E11
Message: PSF SKIPPED ON CLEARED FLAG.
C(MA): 00340
C(AC): I/O status
Cause: The PSF instruction skipped, even though the punch flag was clear.
Recovery: Press CONTINUE. The test will proceed from the point of the error.

Error: E12
Message: PSA DID NOT CLEAR PUNCH FLAG
C(MA): 00227
C(AC): I/O status
Cause: The punch flag was unaffected by the IOT. If it occurs before the test sequence has been scanned, this error, like E10, implies that the punch flag is somehow disconnected and permanently on. If it occurs during the reading of the test tape, however, it probably indicates a random failure.
Recovery: Press CONTINUE. The test will proceed from the point of the error.

Error: E13
Message: PSA FAILED TO SKIP WHEN FLAG WAS SET.
C(MA): 00030
C(AC): I/O status
Cause: This is the other half of the PSF test. In this case, it failed to skip at the proper time.
Recovery: Press CONTINUE. The test will proceed from the point of the error.

Error: E20
Message: NO INTERRUPT AFTER 16.1 MSEC. IS THE PUNCH REALLY ON?
C(MA): 00240
C(AC): I/O status
Cause: The time ran out before an interrupt occurred. The question in the diagnostic message implies that the proper program did not punch anything beyond the blank leader. If the error occurs after some of the sequence has been punched, the failure is probably in the interrupt control.
Recovery: Press CONTINUE. The test will begin again; the title and instructions are printed. When the computer halts, proceed as described in Section 4.4.3.

Error: E21
Message: INTERRUPT FROM SOMETHING OTHER THAN THE PUNCH. CHECK THE OTHER DEVICES....AC SHOWS I/O STATUS. PRESS CONTINUE TO START ALL OVER AGAIN.
C(MA): 00035
C(AC): I/O status
Cause: An interrupt came from some other device than the punch. By examining the I/O status in the AC lights, the offending device can be identified.
Recovery: Press CONTINUE. The test will begin again; the title and instructions are printed. When the computer halts, proceed as described in Section 4.4.3.

Error: E30
Message: None
C(MA): 00325
C(AC): Bits in error
Cause: The character read from tape did not match the one calculated in the program. The bits which did not match are shown in the AC lights as 1's. No message is printed, but the number from the tape and the calculated number are printed in the columns labeled "PUNCHED" and "EXPECTED", respectively.
Recovery: Press CONTINUE. The test will proceed from the point of the error.

4.6 Error Recovery

See Section 4.5, Errors.

5. RESTRICTIONS

Part 1 of this test will not work on any machine equipped with the automatic priority interrupt option because the clock is not connected to register 00007 when the API is installed. The other two tests will work as long as the API is kept off. Since pressing START always disables the API, there should be no interference from it.

6. DESCRIPTION

6.1 Discussion

The three parts of Maindec 712 are all on the same program tape and exist in core together. The operator may switch from one to the other at will, since none interferes with any other.

6.1.1 Part 1 -- Startup Delay Test

If the punch is selected after it has been lying idle, a delay prevents it from being enabled until about 1 second has passed, in order to allow the drive mechanism to engage the clutch. The test of part 1 times this delay; if the punch flag is set before about 1.1 seconds have elapsed, an error message is printed, and the computer halts.

The timing is performed by presetting register 00007 and starting the clock. The punch is selected by a PSA+10 instruction. A short sequence then tests the clock flag and the punch flag in order. As long as the clock has not run out, the link remains off; if the punch flag is set in this interval, the error is noted. When the clock runs out, the link is set; then when the punch flag is raised no error occurs. If the test is successful, a message is printed and the computer stops.

6.1.2 Part 2 -- Scope Waveform Test

This program will punch the character represented by the setting of AC switches 10-17. Switches set up represent 1's. If ACS₀ is up, the program will punch in binary mode the character represented by ACS₁₂₋₁₇. If ACS₁ is up, the program will punch first the character, then its complement, in the mode indicated by ACS₀.

6.1.3 Part 3--Random Sequence Punch

In this part, a sequence of pseudo-random numbers is calculated and punched. The punching can be terminated at any time by raising ACS₁₇. The program keeps a count of the numbers punched, and uses this to determine when the end of the tape is reached while scanning it for errors. The error checking routine uses the same subprogram to calculate each digit, and compares this with the number read from tape.

6.2 APPLICATIONS

6.2.1 Part 1

Part 1 is used when there is reason to suspect trouble in the one-shot startup delay or in the integrating one-shot which holds the punch motor on for 5 seconds after the last select instruction has been processed.

6.2.2 Part 2

Part 2 has several uses. In binary mode, it tests the ability of the punch control to force the proper state of channels 7 and 8. A visual check of the tape as it comes from the punch will show if the logic is functioning correctly.

The operator can visually check the spacing of characters on tape by setting the ACS to 377 and punching in alphanumeric mode. Variations in character spacing can be easily detected. Since every channel is punched, overlapping characters are immediately apparent. With an oscilloscope attached, the operator can examine waveforms generated, as a single character is punched over and over again.

Another symptom of punch malfunctioning appears when residual noise from the punching of one character causes a channel in the succeeding character to be punched, even though it is not conditioned by a 1 in the AC. By repeatedly punching a code and its complement in succession, this type of error can be detected quite readily. Normally, the state of each channel changes every time the punch is selected; the error is revealed when the same channel in adjacent characters is punched.

6.2.3 Part 3

If the action of a punch magnet is being influenced by the action of the ones adjacent to it, a spurious bit may occasionally be punched. The random sequence punched in the test eventually produces every possible combination of bits in a character. It also provides another test of residual noise (see Section 6.2.2) by producing all possible combinations of successive characters, something which is not provided, for example, by a strict numeric sequence. The random sequence is as long as the operator wishes to make it.

7. METHODS

The random sequence punch program keeps a count of the number of characters punched. This count is used by the test program to determine when the end of the sequence has been reached. This count is remembered, so that the same tape can be tested several times as long as the test program remains in memory. A new tape should be punched each time the program is loaded.

8. FORMAT (Not applicable.)

9. EXECUTION TIME

Parts 2 and 3 are indefinite in length. Part 1 takes just over 1 second if the test is successful.

10. PROGRAMS

10.4 Program Listing

Maindec 712: PDP-7 PUNCH TEST

/IN THREE PARTS: 1. STARTUP DELAY TEST
/ 2. SCOPE WAVEFORM TEST
/ 3. RANDOM SEQUENCE PUNCH

/PART 1. STARTUP DELAY TEST

40/

STADT,	TIN	
	LAW STIT	/PRINT TITLE
	TSR	
	LAM -55	/DELAY TIME
	DAC 7	
	IUF	
	CLON	
	PSA+10	
	CLSF	/IS TIME UP?
	SKPVCLL	/NO.
	STL	/YES, SET SIGNAL
	PSF	/PUNCH DONE?
	JMP .-4	/NO.
	SZL	/YES. DID TIME RUN OUT?
	JMP SOUT	/YES. TEST OK.
	CLOF	/NO. PRINT ERROR MESSAGE
	TIN	
	LAW EM00	
	TSR	
	LAC 7	
E00,	HLTVCMA	/ERROR. AC SHOWS TIME LEFT.
	JMP STADT	/TRY AGAIN
SOUT,	TIN	
	LAW SOK	
	TSR	
A,	HLTVCLC	/OK HALT.
	JMP .-1	/CAN'T GET OUT OF IT

/PART 2: SCOPE WAVEFORM TEST. PUNCHES C(AC).

100/

PACS,	TIN	
	LAW ATIT	/PRINT TITLE AND INSTRUCTIONS
	TSR	

B,	CLOF	/WAIT FOR ACS SETTING
PACH,	HLTVCLA	
	LASVCLL	
	SPA	/WHICH MODE?
	STL	/BINARY
PALP,	PSA	/START PUNCH IN ALPHA
	SZL	/WHICH MODE?
	PSB	/BINARY. OVERRIDE PSA.
	PSF	
	JMP .-1	
	RAL	/FORMAT SWITCH
	SMAVRAR	/COMPLEMENT?
	JMP PACH	/NO
	CMA	/YES.
PAL2,	JMP PALP	

/PART 3. RANDOM SEQUENCE PUNCH

200/

RAPT,	DZM NONO	/INITIALIZE COUNTER,
	LAC(1233	/RN SEEDS,
	DAC RAN1	
	LAC (7622	
	DAC RAN2	
	LAC (FLEX CAF	/AND EM10 DIAGNOSTIC
	DAC EPT	

212/ C, *NO P
STOPS MESSAGE* {

	TIN	
	LAW PTIT	
	TSR	
	HLTVCLAVCLL	/WAIT FOR ACS.

	CLOF	
	CAF	/TRY CLEARING FLAGS
	JMS FLAGT	
E10,	HLTVSTL	/ERROR: CAF FAILED. AC SHOWS STATUS
	LAM -300	
	JMS PFEE	/PUNCH LEADER
	LAC (FLEX PSA	
	DAC EPT	/SET DIAGNOSTIC

PRAN,	JMS RANDY	/GET A NUMBER
	ISZ NONO	
	PSA	

E12,	JMS FLAGT	
	HLTVSTL	/ERROR: PSA FAILED. AC SHOWS STATUS
	ION	
	LAM -6000	/INTERRUPT TIMER: 16.1 MSEC.

	DAC APTEM	
	ISZ APTEM	/WAIT LOOP
	JMP .-1	

ER20,	JMS RACS	/ERROR ROUTINE: NO INTERRUPT
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	TYT LAC RAN1 AND (377 JMS OPT JMP RAC2	/PRINT THEORY
E30H,	LAC RAN1 AND (377 XOR APTEM	/SET UP ERROR HALT
E30,	HLTVSTL JMP COUNT	/ERROR: AC SHOWS BAD BITS AS IS
/THE ERROR DETECTIVE		
FLAGT,	0 IORS RTL SPA JMP ER10 PSF JMP FLOUT	/EXAMINE STATUS /IS PUNCH FLAG CLEAR? /NO. /YES. TEST SKIP /OK.
ER11,	JMS RACS LAW EM11	/SKIP FAILED
E11,	HLTVSTL JMP FLOUT	/ERROR: PSF SKIPPED ON CLEAR FLAG
ER10,	JMS RACS JMP F10P JMP I FLAGT	/FLAG NOT CLEAR /GO PRINT MESSAGE /HERE IF HALT
FLOUT,	ISZ FLAGT JMP I FLAGT	/HERE IF NONE
E10P,	LAC EPT TY3 LAW EM10 JMP RAC2-1	/CAF OR PSA

/A NECESSARY ITEM

OTY,	0 TLS TSF JMP .-1 JMP I OTY	/CHAR PRINT SUBROUTINE
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/SOME PUNCHY SUBROUTINES

PAL2+1/

RANDY,	0 CLL LAC RAN1 RTL ADD RAN2	/RANDOM NUMBER GENERATOR
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	DAC RAN1	
	RTL	
	ADD RAN2	
	RTL	
	DAC RAN2	
	LAC RAN1	/THIS IS IT
	AND (377	
	JMP I RANDY	
PFEE,	0	/TAPE FEEDER
	DAC APTEM	
PFEL,	PSA+10	/FEED LOOP
	SZA	/TEST: IS AC CLEAR?
	JMP ER01	/NO
	PSF	/YES.
	JMP .-1	
PFEL+5,	ISZ APTEM	
	JMP PFFL	
	JMP I PFEE	
ER01,	JMS RACS	/ERROR ROUTINE
	LAW EM01	
E01,	HLTVSTL	/ERROR: AC NOT CLEARED
	JMP PFEL+5	
RACS,	0	/THE ACS EXAMINER
	IOF	
	LAS	
	RAL	
	SPAVRAR	/PRINT MESSAGE?
	JMP RAC2+1	/NO
	TIN	
	XCT I RACS	/WILL BE EITHER LAW OR JMP
	TSR	
RAC2,	LAS	
	SPA	/STOP FOR ERROR?
	ISZ RACS	/NO
	ISZ RACS	
	IORS	
	JMP I RACS	
/THE INTERRUPT SERVICE ROUTINE		
0/	0	
	IORS	
	RTL	
	SMA	/IS PUNCH FLAG SET?
	JMP ER21	/NO. SPURIOUS BREAK.
	JMP ISR	/YES.
20/	0	
	HLTVCLC	/CALCATCHER
	JMP .-1	

ISR,	PSF SKP JMP PNEX	/TEST SKIP /FAILED. /OK.
ER13,	JMS RACS LAW EM13	/PSF FAILED.
E13,	HLTVSTL JMP PNEX	/ERROR: AC SHOWS STATUS
ER21,	JMS RACS LAW EM21	/SPURIOUS INTERRUPT
E21,	NOP HLTVSTL JMP RAPT	/ALWAYS HALTS /AC SHOWS STATUS
NON2,	0	/COUNTER FOR PART 3

OTY+5/

/ERROR MESSAGES, NOTICES, ORACLES AND OTHER WISDOM

/TITLES AND INSTRUCTIONS

STII, TEXT -STARTUP DELAY TEST
TURN ON PUNCH POWER AT CONSOLE.

-

ATIT, TEXT /SCOPE WAVEFORM TEST.
ACS0 UP FOR BINARY, DOWN FOR ALPHA.
ACS1 UP IF COMPLEMENTING, DOWN IF NOT.
ACS10-17 FOR CHARACTER PUNCHED.

/

PTIT, TEXT -RANDOM SEQUENCE PUNCH ROUTINE.
ACS0 DOWN FOR ERROR HALTS, UP FOR NONE.
ACS1 DOWN FOR ERROR PRINT, UP FOR NONE.
ACS17 DOWN TO START. PUT UP TO STOP PUNCHING.

-

RTIT, TEXT -READER CHECK OF RANDOM TAPE.
ACS0 AND ACS1 AS FOR PUNCH. ERRORS PRINTED BELOW.

CHARACTER
PUNCHED EXPECTED

-

SOK, TEXT -DELAY OK. TURN OFF PUNCH POWER SWITCH.

-

/ERROR MESSAGES

EM00, TEXT -STARTUP DELAY FAILURE. PRESS CONTINUE TO TRY IT AGAIN.

-

EM01, TEXT -PSA 10 DID NOT CLEAR AC.

-

EM10, TEXT -DID NOT CLEAR PUNCH FLAG.

-

EM11, TEXT -PSF SKIPPED ON CLEARED FLAG.

-

EM13, TEXT -PSF FAILED TO SKIP WHEN FLAG WAS SET.
-
EM20, TEXT -I WAITED 16 MSEC. BUT NO INTERRUPT. IS THE PUNCH REALLY ON?
-
EM21, TEXT -IGOT AN INTERRUPT, BUT NOT FROM THE PUNCH.
CHECK THE OTHER DEVICES. AC SHOWS I/O STATUS.
PRESS CONTINUE TO START ALL OVER AGAIN.

-
START

/CONSTANTS, VARIABLES, ET ALIA

VARIABLES

COSP,	0	/REVERSE FOR CONSTANTS, BELOW
73/		
RAN1,	0	/RANDOM NUMBER PARTS
RAN2,	0	
NONO,	0	/RANDOM NUMBER COUNTERS
APTEM,	0	/ALL-PURPOSE TEMPORARY STORAGE
EPT,	0	/FOR EM10 MNEMONIC
COSP/		/HERE THERE BE CONSTANTS

PAUSE RAPT

A	71
APTEM	76
ATIT	402
B	104
BTATAB	1253
C	212
COSP	1363
COUNT	307
D	254
EM00	634
EM01	657
EM10	670
EM11	702
EM13	715
EM20	733
EM21	760
EPT	77
ER01	151
ER10	342
ER11	336
ER13	26
ER23	235
ER21	32
ER30	304
E00	64
E01	153

E10	216
E10P	347
E11	340
E12	227
E13	30
E20	240
E21	35
E30	325
E30R	322
E30P	313
F	311
FLAGT	327
FLOUT	345
ISR	23
NCT	1217
NONO	75
NON2	37
OCL	1232
OCN	1356
OCS	1233
OCO	1231
OPC	1355
OPR	1340
OPS	1317
OPT	1312
OPO	1332
OPI	1324
OP2	1346
OP3	1353
OTY	353
PACH	105
PACS	100
PALP	110
PAL2	121
PFEE	137
PFEL	141
PNEX	242
PRAN	223
PREC	251
PTIT	457
RACS	155
RAC2	166
RANDY	122
RAN1	73
RAN2	74
RAPT	200
RECH	274
RL6	1212
RTIT	545
SOK	616

SOUT	66
STADT	40
STIT	360
TBC	1360
TCR	101102
TCRA	1247
TDIGIT	101163
TEMY	1361
TEMY:	1357
TIN	101147
TSP	101116
TSR	101174
TTAB	10
TYEXIT	601206
TYSVAC	1362
TYT	101130
TY1	101036
TY1A	1044
TY1B	1234
TY1C	1243
TY1D	1241
TY2	1066
TY3	101070
XIT	100000
TTAB	10
ISR	23
ER13	26
E13	30
ER21	32
E21	35
NON2	37
STADT	40
E00	64
SOUT	66
A	71
RAN1	73
RAN2	74
NONO	75
APTEM	76
EPT	77
PACS	100
R	104
PACH	105
PALP	110
PAL2	121
RANDY	122
PFEE	137
PFEL	141
ER01	151
E01	153

RACS	155	EM21	760
RAC2	166	TY1A	1044
RAPT	200	TY2	1066
C	212	RL6	1212
E10	216	NCT	1217
PRAN	223	OCU	1231
E12	227	OCL	1232
ER20	235	OCS	1233
E20	240	TY1B	1234
PNEX	242	TY1D	1241
PREC	251	TY1C	1243
D	254	TCRA	1247
RECH	274	BTATAB	1253
ER30	304	OPT	1312
COUNT	307	OPS	1317
F	311	OPI	1324
E30P	313	OP0	1332
E30H	322	OPM	1340
E30	325	OP2	1346
FLAGT	327	OP3	1353
ER11	336	OPC	1355
E11	340	OCN	1356
ER10	342	TEMY!	1357
FLOUT	345	TBC	1360
E10P	347	TEMY	1361
OTY	353	TYSVAC	1362
STIT	360	COSP	1363
ATIT	402	XIT	100000
PTIT	457	TY1	101036
RTIT	545	TY3	101070
SOK	616	TCR	101102
EM00	634	TSP	101116
EM01	657	TY1	101130
EM10	670	TIN	101147
EM11	702	TDIGIT	101163
EM13	715	TSR	101174
EM20	733	TYEXIT	601206

11. DIAGRAMS (Not Applicable)

12. REFERENCES (Not Applicable)